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# Terry Morris

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Terry Morris reports to the HP Servers CTO office within the HP Enterprise Group (EG), where he works with HP technologists, product groups, HP Labs researchers, and industry consortia to develop the high-speed photonic and electronic data communication paths that are critical to EG product performance.

Morris also works across HP business units to evaluate emerging technologies for use in future HP products and hosts HP's High Speed Signaling Forum, which serves to advance the state of the art for electrical and photonic signaling by teaming product developers, board designers, chip designers, and lead technologists with HP Labs researchers to address present challenges and set future direction. Additionally, he is Vice Chair of the HP Servers Technical Career Path (TCP) review board, and provides guidance to TCP candidates, managers, and organizations as needed.

Previously Morris was a technical leader of the HP Superdome program, working with engineering teams to resolve multiple electrical signaling and interconnect challenges. He has also worked across business boundaries to address electrical and photonic product design challenges for a variety of HP servers.

Before joining HP, Morris developed high-speed signaling and interconnects for a variety of supercomputer applications at Convex Computer Company, including the world's first production gallium-arsenide supercomputer. He also helped found the company's first cross-organizational signal integrity group, sharing the best practices required to increase the volume of products developed with limited resources.

In 2008, Morris was recognized as conference chair for DesignCon, an annual industry event specializing in signal integrity and high-speed design. He has also served Board of Directors of the MIT Microphotonics Consortium, an industry group advancing the state of the art in photonic communications. He currently holds over 50 patents, is the author of "Breaking Free of Electrical Constraints" in Applied Physics A, and was co-author of the IEEE technical paper, "A High-speed Optical Multi-drop Bus for Computer Interconnections."