



Kirk Bresniker

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Kirk Bresniker is Chief Architect at HP Labs, where he leads the HP global research group in system hardware and software architecture research and advanced development. Prior to this role, he was Vice President and Chief Technologist of the HP Servers global business unit, overseeing a portfolio that included HP Moonshot, HP ProLiant servers, HP BladeSystem, and mission-critical computing with the HP Integrity platform.

Starting in 1997, Bresniker began a decade-long research and development effort to develop novel modular system architectures that would become a new category of integrated hardware and software offerings known as Blade Servers. This early work was eventually refined and blended with contributions from the HP-Compaq merger to become the HP c-Class BladeSystem, now the undisputed leader in Blade Server platforms.

Both prior to and during his work on Blade Servers, Bresniker was a key contributor to HP's entry level and mid-range PA-RISC and Integrity SMP systems and he continues to be a member of the company's Business Critical Systems team. Additionally, Bresniker was the design lead for HP's first entry-level multi-processor system and for the entire D-class PA-RISC product family, and he was the key architect for all of HP's PA-RISC and Itanium entry- and mid-range servers, overseeing a complete re-vamp of the product line and helping prepare for the PA-RISC to Itanium transition.

Bresniker has also been a lead contributor to HP's industry standard presence in telecommunications. He was a primary contributor to the Infiniband Carrier Grade Focus group and a lead author of the PICMG AdvancedTCA family of standards.

Bresniker holds 27 US and 10 foreign patents in modular platforms and blade systems, integrated circuits, and power and environmental control. He received his BSEE from Santa Clara University, where he was also a member of the Humanities Honors program.