



Craig Walrath

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Craig Walrath is the strategic processor/system-on-a-chip (SoCs) Technical Lead in HP's Printing and Personal Systems (PPS) group, working with major semi-conductor manufacturers on future processor and chipset development.

Based in the PPS Office of the CTO, Walrath supports the development of new technical strategies, investigates key technologies and industry Initiatives, and reviews proposals for new HP products and solutions. He is also a cross-PPS Intellectual Property leader, providing guidance for multiple IP-related efforts, and is a PPS representative on the company-wide HP Patent Investment Review Board.

An 18-year veteran of HP and Compaq, Walrath has served as chair of his business group's Technical Career Path (TCP) board, and is a member of the HP Corporate TCP board. Before joining the PPS CTO organization, Walrath held a variety of technical leadership positions at both companies, including Chief Technologist for Consumer PCs, Commercial and Consumer Notebook Chief Technologist, and Director of Architecture & Technology for Consumer IT.

Prior to Compaq, Walrath was one of the primary architects for Symmetrical Multiprocessor Servers at AT&T/NCR and held director of systems architecture, management, and senior technologist roles designing servers and commercial desktop and notebook PCs. He also worked closely with Microsoft on initial development of Windows NT to run on Symmetrical Multi-Processing servers and worked in GE's R&D organization developing hardware and software for multi-processor systems.

Walrath holds a Master's degree in electrical and computer engineering from Clarkson University.