



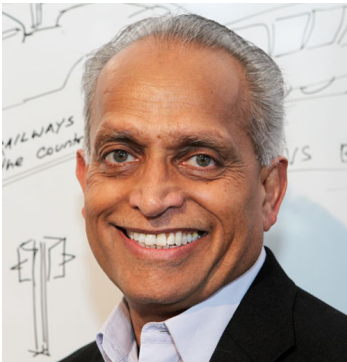
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### **Executive Biography**

## **Chandrakant D. Patel P.E.**

### **HP Chief Engineer and Senior Fellow**

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Chandrakant Patel is HP's Chief Engineer and Senior Fellow.

Chandrakant has led HP in delivering innovations in chips, systems, data centers, storage, networking, print engines and software platforms. He is a pioneer in thermal and energy management in data centers, and in the application of information technology to drive available energy management at the scale of cities.

Chandrakant is an ASME and an IEEE Fellow, has 153 patents and has published more than 150 technical papers. An advocate of return to fundamentals, he has served as an adjunct faculty in engineering at Chabot College, U.C. Berkeley Extension, San Jose State University and Santa Clara University. In 2014, Chandrakant was elected into the Silicon Valley Engineering Hall of Fame. In 2018, he was elected to the National Academy of Engineering.

Chandrakant started his collegiate education in a community college and has immense passion for training and inclusion programs for under-served communities. He believes that companies architecting solutions for the 21st century cyber physical age should be socially responsible and lead with purpose, making life better for everyone, everywhere.