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Executive Biography

Steven Miller

Chief Technologist of Graphics Solutions Business
HP Fellow & Vice President
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Steven Miller is an HP Fellow, Vice President, and Chief Technologist of HP's Graphics Solutions Business, where he focuses on developing future technologies across the graphics markets. Steven has 31+ years' experience in the research and development of imaging and printing technologies at HP, with an emphasis on digital imaging systems, printing technologies and architectures, and product system architecture development.

Previously, Steven was the Chief Technologist of Imaging and Printing, and before that strategist and system architect for HP's Enterprise PageWide products. Miller was also the system architect for Edgeline, HP's first page-wide array printing platform, and led a pan-printing technology platform analysis that outlined the role scanning inkjet, laser, PageWide, and liquid-EP technologies should play across the entire market space.

Steven has been the system architect for numerous HP imaging and printing platforms including: personal and office inkjet printing systems; photofinishing systems for both retail and central lab photo fulfillment; digital imaging architectures shared across the Imaging and Printing Group; and an E-Beam Lithography imaging architecture during the early phases of a joint development with a major silicon foundry partner.

He is a founding member of HP's first core imaging technology development group, and leader of the High Performance Architecture team that redefined HP's inkjet printer imaging architecture in the late 1990's. He has contributed to many of the printing industry's most significant innovations, such as the HP DeskWriter, the industry's first host-based page rendering printing system for a modern graphics-based OS and the DeskJet and DeskWriter 500c, the industry's first plain paper full color printers.

Miller joined HP upon graduating from college, as a firmware engineer for the RuggedWriter impact printer. He has been a program committee member and co-chair for HP Tech Con, HP's primary vehicle for sharing technical work and generating new ideas across disciplines. He also has significant experience in bringing new technologies to market and is a named author of 13 patents, including HP's key High Performance Architecture and HP ColorSmart patents. He holds a B.S. in electrical & computer engineering and a B.S. in computer science from Oregon State University.